

## EAST - [10.230750 compound DLL.wsp:1]

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- BRS:
- BRS:
- Pending
- Active
  - L17: (15257) variable adj2 delay\$4
  - L18: (78748) frequency near2 divid\$4
  - L19: (63974) phase adj2 detect\$5
  - L20: (55251) (feedback or feed-back) adj2 loop
  - L21: (4354) 327/15?
  - L22: (4987) 365/233
  - L23: (4797) 365/189.05
  - L24: (211682) 365/
  - L25: (198959) 327/
  - L26: (34) (Harrison near2 Ronnie).in.
  - L27: (9696) dll or (delay\$4 adj2 lock\$4 adj2 loop)
  - L29: (890) 27 and 17
  - L30: (215) 29 and 18
  - L31: (93) 30 and 19
  - L32: (27) 20 and 31
  - L74: (205) 29 and 24
  - L75: (436) 29 and 25
  - L76: (133) 30 and 25
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 Default operator: OR  Highlight all hit terms initially

BRS term  S&R term  Image  Text  HTML

	U	1	2	Document ID	S	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cls	Inventor	S
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5939913 A	U	19990817	18	DLL circuit and semiconductor memory device using same	327/158	327/159; 327/236;		Tomita, Hiroyoshi	<input checked="" type="checkbox"/>
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5973525 A	U	19991026	23	Integrated circuit device	327/158	327/149; 327/162;		Fujii, Yasuhiro	<input checked="" type="checkbox"/>
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6194932 B1	U	20010227	29	Integrated circuit device	327/158	327/141; 327/149;		Takemae, Yoshihiro et al.	<input checked="" type="checkbox"/>
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6351166 B1	U	20020226	15	Semiconductor device with stable and appropriate data output timing	327/158	327/244; 327/276		Hoshimoto, Yukinori	<input checked="" type="checkbox"/>
5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6476653 B1	U	20021105	26	DLL circuit adjustable with external load	327/158	327/161; 331/1A		Matsuzaki, Yasurou	<input checked="" type="checkbox"/>

Hits  Details  HTML

Ready

NUM

Start Cli... Inb... eD... Ex... Ra... Wi... Do... e... 1... MEA... MEA... ENP 1:47 PM